IN THE SPECIFICATION:

Please replace the paragraph beginning at page 15, line 25, with the following rewritten paragraph:

With respect to a D flip-flop 1401 (as shown in FIG. 14) used to implement the storage device 909 in the lowpass embodiment of the converter 900, the lowpass-to-bandpass transformation requires a D flip-flop 1401 (as shown in FIG. 14) having its output at the non-inverting Q terminal in the lowpass design to transform into two serially connected D flip-flops 1901, 1902, wherein the first D flip-flop 1901 has its output at the non-inverting Q terminal and the second D flip-flop 1902 has its output at the inverting Q terminal. During a first clock cycle, the first D flip-flop 1901 produces an intermediate output signal from its non-inverting Q terminal, which is then stored by the second D flip-flop 1902 until the next clock cycle. During the next clock cycle, the second flip-flop 1902 produces the clocked output signal 931 from the flip-flop's inverting Q terminal. Thus, the output of the inverting Q terminal of the second D flip-flop 1902 comprises the clocked output signal 931 of the storage device 909 for the newly formed bandpass sigma-delta converter. To achieve improved signal-to-noise performance in a bandpass sigma-delta converter in accordance with the present invention, at least one instability generator 911 must be added in the feedback loop as in the lowpass sigma-delta converter 900. Thus, as shown in FIG. 19, when the storage device 909 itself comprises two D flip-flops 1901, 1902 and the instability generator 911 is implemented by a D flip-flop 1904, the D flip-flop 1904 implementing the instability generator 911 is in addition to any D flip-flops 1901, 1902 that are used to implement the storage device 909. As noted above, other converter components, such as

the filters 904, 905, must also be transformed in accordance with known techniques to implement a bandpass sigma-delta converter. A bandpass sigma-delta converter with preferred implementations of the filters 904, 905 is described in detail in U.S. Patent No. 5,768,315, which is incorporated herein by this reference as if fully set forth herein.